

REMARKS

Favorable reconsideration of this application as presently amended and in light of the following discussion is respectfully requested.

Claims 1, 3-5, 7 and 8 are pending in the present application. Claims 2, 6 and 9-13 have been cancelled and claims 1, 3-5, 7 and 8 have been amended by the present amendment.

In the outstanding Office Action, claims 9-13 were rejected under 35 U.S.C. § 101; and claims 1-13 were rejected under 35 U.S.C. § 103 (a) as unpatentable over Uchida et al. in view of Takeuchi et al.

Applicants thank the Examiner for discussing this application with Applicants' representative on October 10, 2007. During the discussion, the differences between the present invention and the prior art were discussed. No agreement was reached pending the Examiner's further review when a response is filed. Comments presented during the discussion are reiterated below.

Regarding the rejection of claims of 9-13 under 35 U.S.C. § 101, claims 9-13 have been cancelled. Accordingly, it is respectfully requested this rejection be withdrawn.

Claim 1-13 stand rejected under 35 U.S.C. § 103 (a) as unpatentable over Uchida et al. in view of Takeuchi et al. This rejection is respectfully traversed.

Independent claim 1 includes a combination of elements and has been amended to include subject matter similar to that recited in dependant claim 2. In particular, dependent claim 1 is been amended to clarify that the video signal packet conversion unit includes a video signal characteristic recognizing unit configured to recognize video signal characteristics from an input

characteristic signal of a video signal and to generate a video signal characteristic packet, a video signal control unit configured to directly receive the video signal characteristics from the video signal characteristic recognizing unit and to generate a header and a tail of a video signal based on the received video signal characteristics, input horizontal/ vertical synchronization signals and a video clock signal and to simultaneously output a video signal, a video signal memory unit configured to directly receive the video signal output by the video signal control unit and to store video signal, and a multiplexer directly connected to the video signal control unit and the video signal memory unit and configured to select a header and a tail output by the video signal control unit and a video signal output by the video signal memory unit to thus generating a video signal packet. Independent claim 5 include similar features in a varying scope.

These features are supported at least by FIG. 3 and the corresponding description in the specification. For example, as shown in FIG. 3, the video signal packet conversion unit 300 includes a video signal characteristic recognizing unit 301 configured to recognize video signal characteristics from an input characteristic signal of a video signal and to generate a video signal characteristic packet, a video signal control unit 303 configured to directly receive the video signal characteristics from the video signal characteristic recognizing unit 301 and to generate a header and a tail of a video signal based on the received video signal characteristics, input horizontal/ vertical synchronization signals and a video clock signal and to simultaneously output a video signal, a video signal memory unit 305 configured to directly receive the video signal output by the video signal control unit and to store video signal, and a multiplexer 307 directly connected to the video signal control unit 303 and the video signal memory unit 305 and

configured to select a header and a tail output by the video signal control unit 303 and a video signal output by the video signal memory unit 305 to thus generating a video signal packet.

The Office Action relies on Takeuchi et al. as disclosing a device for a serial digital interface transmission/reception and cites FIG. 4. However, as discussed during the interview, the circuit in FIG. 4 of Takeuchi et al. does not include the video signal characteristic recognizing unit, the video signal control unit, and the video signal memory unit that have direct connections between each other as in the present invention. Uchida et al. also do not teach or suggest these features.

Accordingly, it is respectfully submitted independent claims 1 and 5 and each claims depending therefrom are allowable.

Conclusion

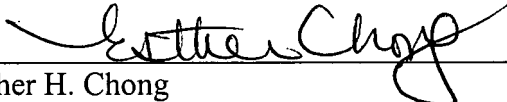
In view of the above remarks, it is believed that claims are allowable.

Should there be any outstanding matters that need to be resolved in the present application, the Examiner is respectfully requested to contact David A. Bilodeau Reg. No. 42,325 at the telephone number of the undersigned below, to conduct an interview in an effort to expedite prosecution in connection with the present application.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37.C.F.R. §§1.16 or 1.14; particularly, extension of time fees.

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Respectfully submitted,

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